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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	March 11, 2004
		First Named Inventor	Surgey Sukhobok, et al.
		Art Unit	
		Examiner	
		Attorney Docket No.	16811MDUS01U
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U.S. PATENT DOCUMENTS				
Examiner's Initials	Citation Number	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document
	A1			
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FOREIGN PATENT DOCUMENTS				
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OTHER PRIOR ART -- NON-PATENT LITERATURE DOCUMENTS		
Examiner's Initials	Citation Number	Document Description -- Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
ell	C1	R. ECHARD, et al., <i>The <math>\pi</math>-Rotation Low-Density Parity Check Codes</i> , IEEE publication 0-7803-7206-9/01, 2001, pp. 980-984
ell	C2	R. ECHARD, et al., <i>Irregular <math>\pi</math>-Rotation LDPC Codes</i> , IEEE publication 0-7803-7632-3/02, 2002, pp. 1274-1278
ell	C3	R. ECHARD, et al., <i>The Extended Irregular <math>\pi</math>-Rotation LDPC Codes</i> , IEEE communications letters, Vol. 7, No. 5, May 2003, pp. 230-232
ell	C4	R. ECHARD, et al., <i>Deterministic <math>\pi</math>-Rotation Low-Density Parity Check Codes</i> , Electronics Letters, Vol. 38, No. 10, May 2002, pp. 464-465
ell	C5	D. HOCEVAR, <i>LDPC Code Construction with Flexible Hardware Implementation</i> , IEEE publication 0-7803-7802-4/03, 2003, pp. 2708-2712
ell	C6	H. ZHONG, et al., <i>Design of VLSI Implementation-Oriented LDPC Codes</i> , (4 pages)
ell	C7	R. ECHARD, <i>On the Construction of Some Deterministic Low-Density Parity-Check Codes</i> , Ph.D. dissertation, 2002, (162 pages)

Examiner Signature	<i>ell</i>	Date Considered	12/19/06
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